Product Specification

AGO-043J2-NN-R

Display TFT 4,3" 480 x 272 com touch resistivo 4 fios

Composição deste part number

Display	AGO-043J2-NN-N	páginas 2 a 21
Touch	AGT-043J0-01	página 22

As páginas seguintes são o datasheet do display e o desenho do touch,
sem alteração. A especificação do display vale para todas as versões AGO-043J2-NN.

Product Specification

4.3" TFT LCD Module

Model No :	**AGO_043J2-NN-N**
Resolution:	480*272
Interface:	RGB 40PIN
Luminance:	400cd/m²

〈 ◇〉 Preliminary Specification

〈 ◆〉 Finally Specification

CUSTOMER'S APPROVAL	
CUSTOMER :	
SIGNATURE:	DATE:

APPROVED BY	PM REVIEWED	PD REVIEWED	PREPARED BY

Record of Revision

Version	Revise Date	Page	Content
Pre-spec .01	2021/01/28		Initial Release.

Contents

1. General Specifications 4
2. Pin Assignment 6
2.1. TFT LCD PanelDriving Section 6
3. Operation Specifications 8
3.1. Absolute Maximum Ratings 8
3.2. Typical operation conditions 9
3.3 Backlight Driving Conditions 9
3.4. Power Sequence 10
3.5. Timing Characteristics 11
3.5.1. Timing Conditions 11
3.5.2Timing Diagram 12
4. Optical Specifications 13
5. Reliability Test Items 17
6. General Precautions 18
7.1. Safety 18
7.2. Handling 18
7.3. Static Electricity 18
7.4. Storage 18
7.5. Cleaning 18
8. Mechanical Draw 19
9. Package Drawing 20
9.1. Packaging Material Table 20
9.2. Packaging Quantity 20

1. General Specifications

No.	Item	Specification	Remark
1	LCD size	4.3 inch(Diagonal)	
2	Driver element	a-Si TFT active matrix	
3	Resolution	480 × 3 (RGB) × 272	
4	Display mode	Normally White, Transmissive	
5	Dot pitch	0.066(W) × 0.198(H) mm	
6	Active area	95.04(W) × 53.856(H) mm	
7	Module size	105.4(W) × 67.07(H) ×3.0(D) mm	Note 1
8	Surface treatment	Anti-Glare	
9	Color arrangement	RGB-stripe	
10	Interface	Digital	
11	Backlight Power consumption	TBD W(Typ.)	
12	Panel Power consumption	TBD W (Typ.)	
13	Weight	TBD (Typ.)	

Note 1: Refer to Mechanical Drawing.

2. Pin Assignment

2.1. TFT LCD Panel Driving Section

FPC Connector is used for the module electronics interface. The recommended model is manufactured by Hiro es.

Pin No.	Symbol	I/O	Function	Remark
1	V_{LED-}	P	Power for LED backlight cathode	
2	V_{LED+}	P	Power for LED backlight anode	
3	GND	P	Power ground	
4	V_{DD}	P	Power voltage	
5	R0	I	Red data (LSB)	
6	R1	I	Red data	
7	R2	I	Red data	
8	R3	I	Red data	
9	R4	I	Red data	
10	R5	I	Red data	
11	R6	I	Red data	
12	R7	I	Red data (MSB)	
13	G0	I	Green data (LSB)	
14	G1	I	Green data	
15	G2	I	Green data	
16	G3	I	Green data	
17	G4	I	Green data	
18	G5	I	Green data	
19	G6	I	Green data	
20	G7	I	Green data (MSB)	

21	B0	I	Blue data (LSB)	
22	B1	I	Blue data	
23	B2	I	Blue data	
24	B3	I	Blue data	
25	B4	I	Blue data	
26	B5	I	Blue data	
27	B6	I	Blue data	
28	B7	I	Blue data (MSB)	
29	GND	P	Power ground	
30	CLK	I	Pixel clock	
31	DISP	I	Display on/off	
32	HSYNC	-	HORIZONTAL SYNC INPUT IN RGB MODE	
33	VSYNC	-	VERTICAL SYNC INPUT IN RGB MODE	
34	DEN	I	DATA ENABLE	
35	NC	-	NC	
36	GND	P	GROUND	
37	XR(NC)	-	TOUCH PLANE PIN/NC	
38	YD(NC)	-	TOUCH PLANE PIN/NC	
39	XL(NC)	-	TOUCH PLANE PIN/NC	
40	YU(NC)	-	TOUCH PLANE PIN/NC	

I:input,O:output,P:

3. Operation Specifications

3.1Absolute Maximum Ratings

Item	Symbol	Values		Unit	Remark
		Min.	Max.		
Power voltage	V_{DD}	-0.5	5.0	V	
Input signal voltage	Logic input	-0.5	5.0	V	
Operation temperature	T_{OP}	-20	70	℃	
Storage temperature	T_{ST}	-30	80	℃	
LED Reverse Voltage	V_R	-	3.2	V	Each LED Note 2
LED Forward Current	I_F	-	25	mA	Each LED

Note 1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. A module should be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme condition, the module may be permanently destroyed.

Note 2: V_R Conditions: Zener Diode 20m

3.2. Typical operation conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Power voltage	V_{DD}	3.1	3.3	3.5	V	
Current for Driver	IV_{DD}	-	TBD	25	mA	V_{DD} = 3.3V
Inputlogichigh voltage	V_{IH}	$0.8V_{DD}$	-	V_{DD}	V	Note 1
Input logic low voltage	V_{IL}	GND	-	$0.2V_{DD}$	V	

Note1: CLK, DE, R0~ R7, G0~ G7, B0~ B7.

3.3 Backlight Driving Conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Voltage for LED Backlight	V_L		15	16	V	Note 2
Current for LED Backlight	I_L	–	40	–	mA	
LED life time	-	30,000	-	-	Hr	Note 1

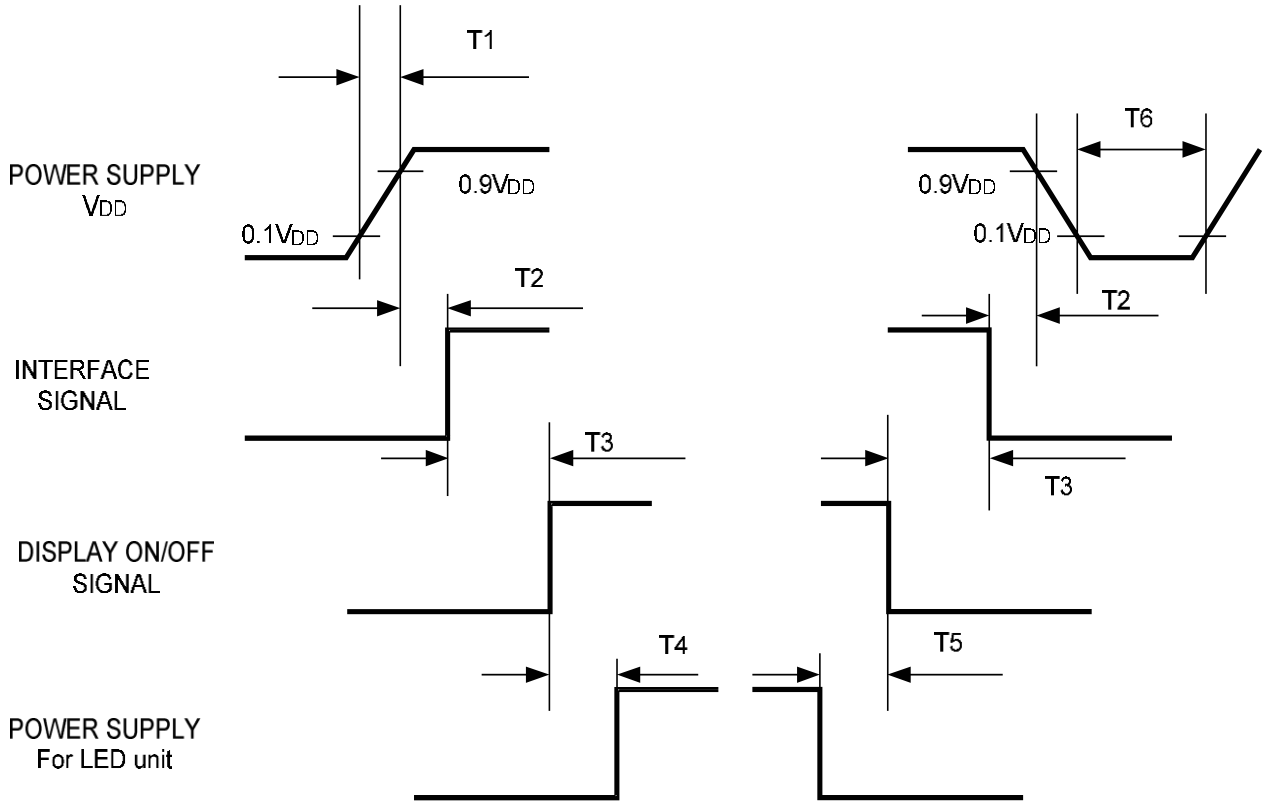
Note 1: The " LED life time" is defined as the module brightness decrease to 50% original brightnessthatthe ambienttemperature is 25℃ and I_L =40mA. The LED lifetime could be decreased if operating I_L is lager than 40mA.

Note 2: The LED Supply Voltage is defined by the number of LED at Ta=25℃ and I_L =40mA.

.

3.4. Power Sequence

To preventalatch-upor DCoperationofthe LCDmodule,thepoweron/offsequence should be as the diagram below.



Symbol	Specification	Symbol	Specification
T1	0≦ T1≦ 10msec	T4	160 msec ≦ T4
T2	0≦ T2≦100 msec	T5	160 msec ≦ T5
T3	0≦ T3≦200 msec	T6	1 msec ≦T6

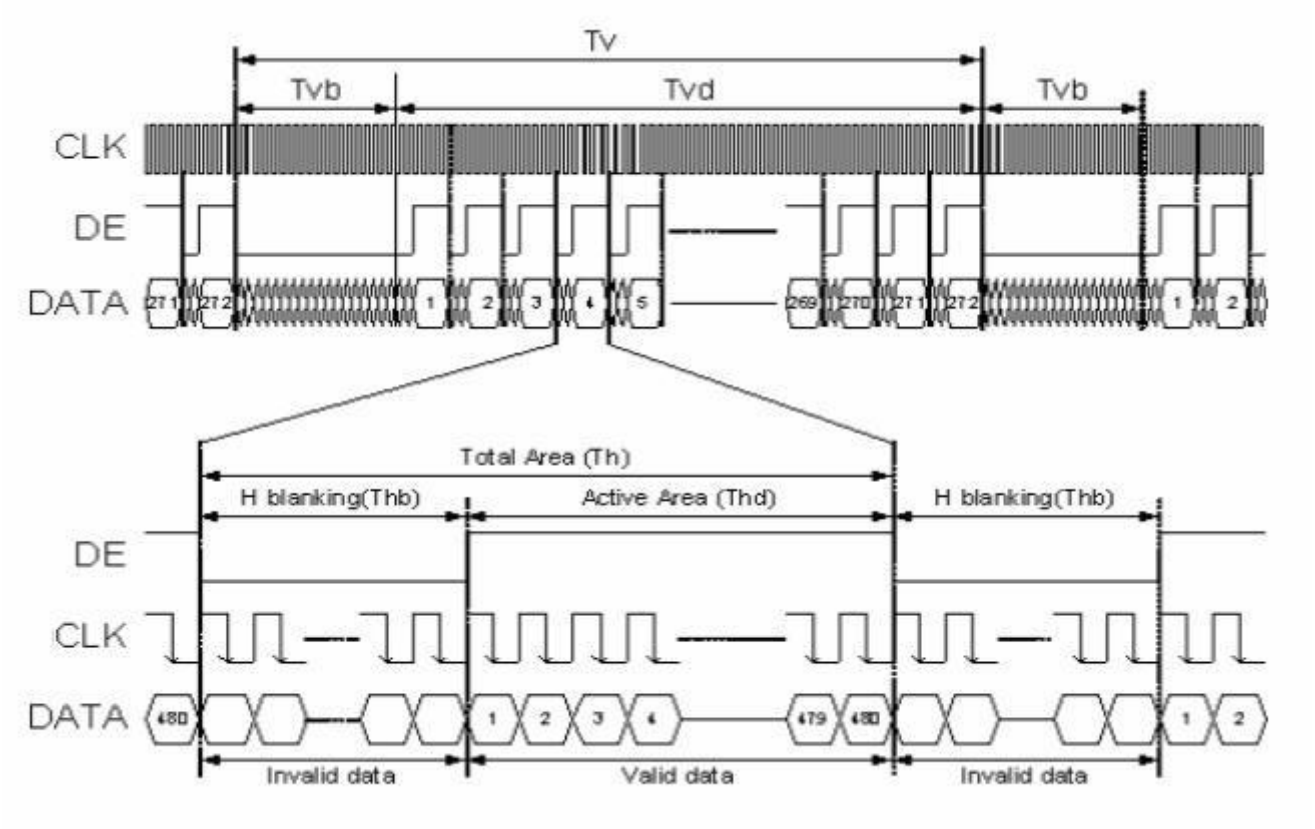
3.5. Timing Characteristics

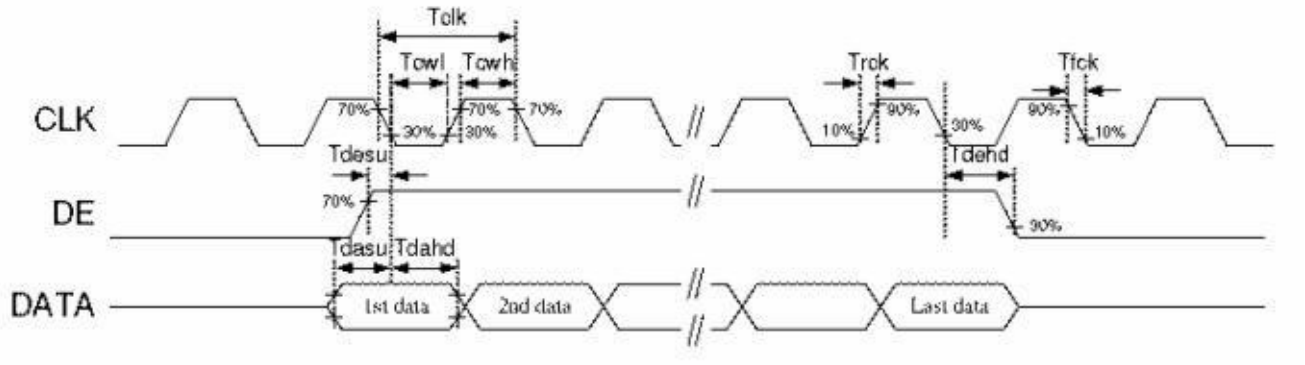
3.5.1. Timing Conditions

Parallel DE mode RGB input timing table

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
CLK frequency	fclk	7	9	12	MHz
DEV period time	Tv	277	288	400	H
DEV display area	Tvd		272		H
DEV blanking	Tvb	5	16	128	H
DEH period time	Th	520	525	800	CLK
DEH display area	Thd		480		CLK
DEHblanking	Thb	40	45	320	CLK
CLK cycle time	Tclk	83	110	143	ns
Clock width of high level	Tcwh	40	50	60	%
Clock width of low level	Tcwl	40	50	60	%
Clock risingtime	t_{rck}		-	9	ns
Clock fallingtime	t_{fck}		-	9	ns
Data Setup Time	t_{desu}	10	-	-	ns
Data Hold Time	t_{dahd}	10	-	-	ns
DE Setup Time	t_{desu}	10	-	-	ns
DE HoldTime	t_{dehd}	10	-	-	ns

3.5.2. Timing Diagram





4. Optical Specifications

Item	Symbol	Condition	Values			Unit	Remark
			Min.	Typ.	Max.		
Viewing angle (CR≥ 10)	θ_L	Φ =180°(9o' clock)	60	70	-	degree	Note 1
	θ_R	Φ =0°(3o' clock)	60	70	-		
	θ_T	Φ =90°(12o' clock)	40	50	-		
	θ_B	Φ =270°(6o' clock)	60	70	-		
Response time	T_{ON}	Normal θ=Φ=0°	-	10	20	msec	Note 3
	T_{OFF}		-	15	30	msec	Note 3
Contrast ratio	CR			500	-	-	Note 4
Color chromaticity	W_X		0.26	0.31	0.36	-	Note 2 Note 5 Note 6
	W_Y		0.28	0.33	0.38	-	
Luminance	L		350	400	-	cd/m2	Note 6
Luminanceuniformity	Y_U		70	75	-	%	Note 7

Test Conditions:

1. V_{DD}=3.3V, I_L=40mA (Backlight current), the ambient temperature is 25℃.
2. The test systems refer to Note 2.

Note 1: Definition of viewing angle range

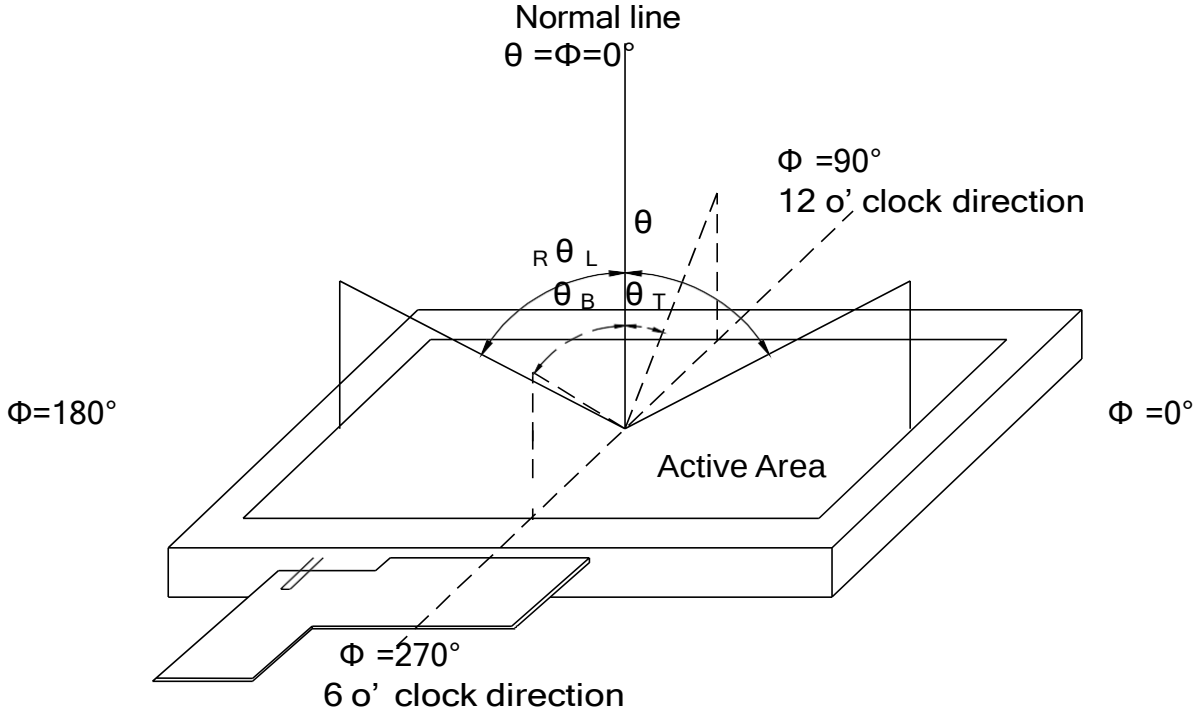


Fig. 4-1 Definition of viewing angle

Note 2: Definition of optical measurement system.

Theopticalcharacteristicsshouldbemeasuredindarkroom.After 30 minutes operation, the optical propertiesare measuredat the center pointofthe LCD screen.(Responsetimeismeasuredby Photodetector TOPCONBM-7,other items are measured by BM-5A/Field of view: 1° /Height: 500mm.)

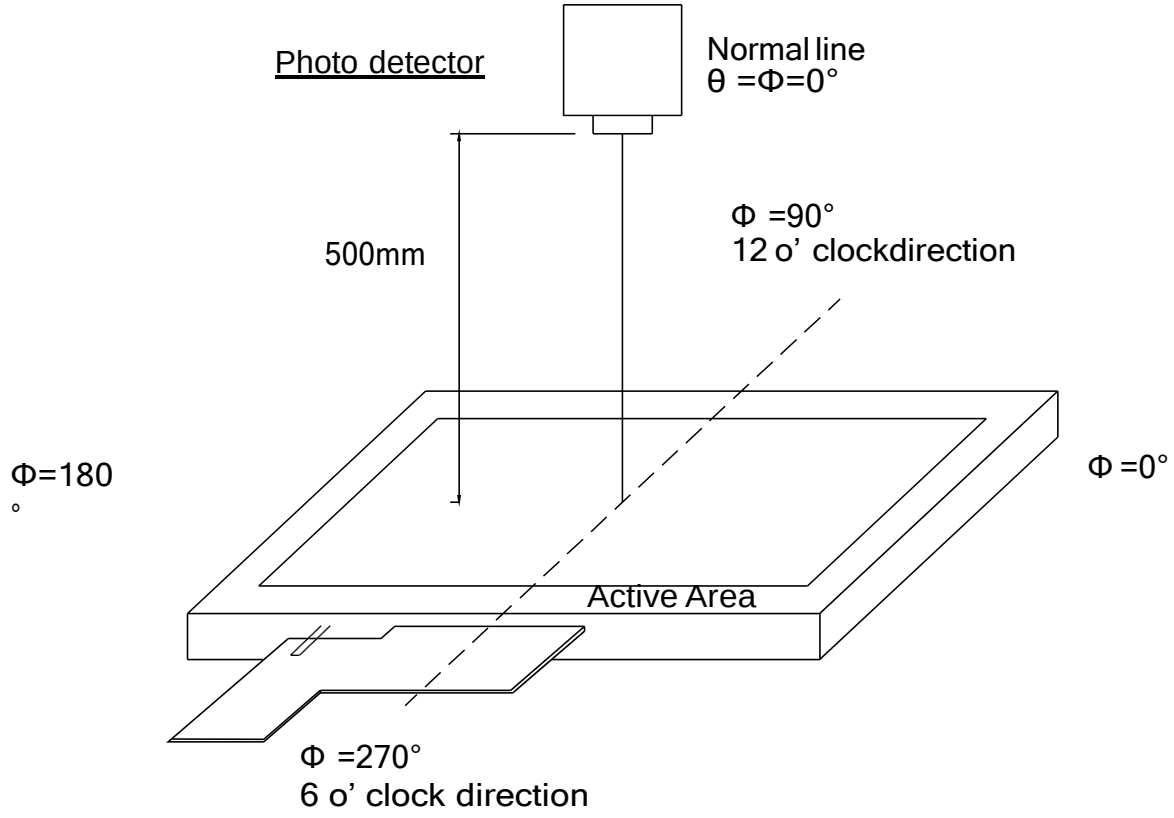


Fig. 4-2 Optical measurement system setup

Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between " White" state and " Black" state. Rise time (T_{ON}) is the time between photodetector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.

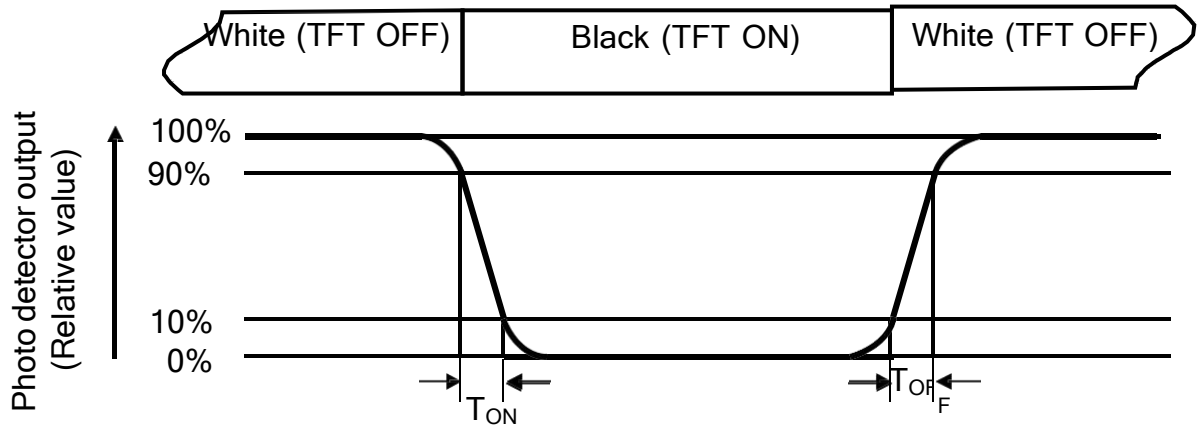


Fig. 4-3 Definition of response time

Note 4: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the " White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel. The LED driving condition is I_L=40mA.

Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuringareas(Refer to Fig. 4-4).Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity (Yu)} = \frac{B_{min}}{B_{max}}$$

L-------Active area length W----- Active area width

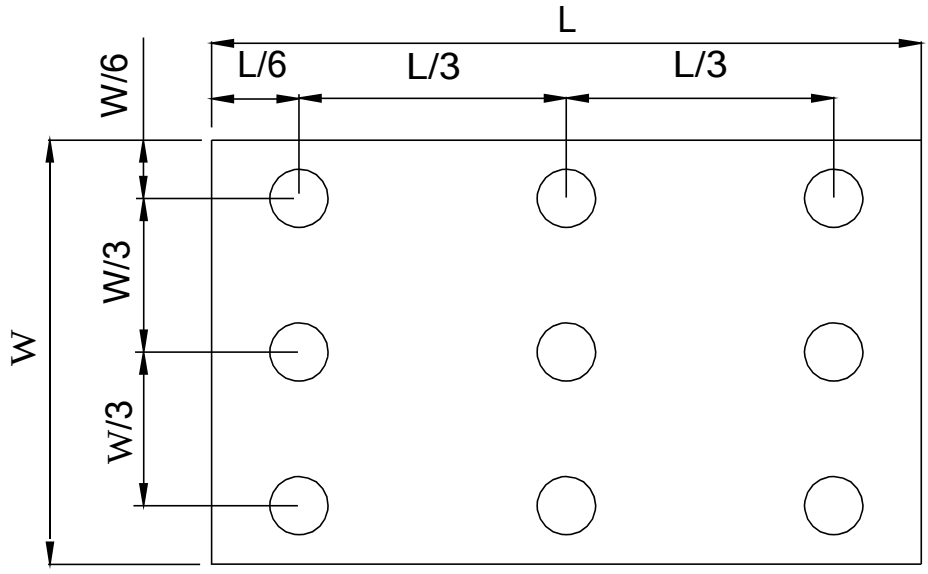


Fig. 4-4 Definition of measuring points

B_{max}: The measured maximum luminance of all measurement position.
B_{min}: The measured minimum luminance of all measurement position.

5. Reliability Test Items

(Note3)

Item	Test Conditions	Remark
High Temperature Storage	Ta = 80℃ 240 hrs	Note 1,Note 4
Low Temperature Storage	Ta = -30℃ 240hrs	Note 1,Note 4
High Temperature Operation	Ts = 70℃ 240hrs	Note 2,Note 4
Low Temperature Operation	Ta = -20℃ 240hrs	Note 1,Note 4
Operate at High Temperature and Humidity	+60℃, 90%RH 240 hrs	Note 5
Thermal Shock	-20℃/30 min~+70℃/30 minforatotal 100 cycles, Startwith cold temperature and end with high temperature	Note 4
Vibration Test	Frequency range:10~55Hz Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X. Y.Z. (6 hours for total)	
Mechanical Shock	100G 6ms,±X, ±Y, ±Z 3 times for each direction	
Package Vibration Test	Random Vibration : 0.015G*G/Hz from 5-200HZ, -6dB/Octave from 200-500HZ 2 hours for each direction of X. Y.Z. (6 hours for total)	
Package Drop Test	Height:60 cm 1 corner, 3 edges, 6 surfaces	
Electro Static Discharge	± 2KV, Human Body Mode, 100pF/1500Ω	

Note 1: Tais the ambienttemperature ofsamples.

Note 2: Tsis the temperature ofpanel' s surface.

Note 3: Inthestandardcondition, thereshallbenopracticalproblemthatmayaffectthe display function. After the reliability test, the product only guarantees operation, but doesn' t guarantee all the cosmetic specification.

Note 4: Before cosmetic and function tests, the product must have enough recovery time, at least 2 hours at room temperature.

6. General Precautions

7.1. Safety

Liquidcrystalispoisonous.Donotputitin yourmouth. Ifliquidcrystaltouches your skin or clothes, wash it off immediately by using soap and water.

7.2. Handling

1. The LCDpanelisplateglass. Donotsubjectthe paneltomechanical shockorto excessive force on its surface.
2. Thepolarizer attached tothe display is easilydamaged. Pleasehandleit carefully to avoid scratch or other damages.
3. Toavoidcontaminationonthedisplaysurface,donottouchthemodulesurface with bare hands.
4. Keep a space so that the LCD panels do not touch other components.
5. Putcover board such asacrylicboardon the surface of LCD panel toprotect panel from damages.
6. Transparentelectrodesmaybedisconnectedifyouusethe LCDpanelunder environmental conditions where the condensation of dew occurs.
7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

7.3. Static Electricity

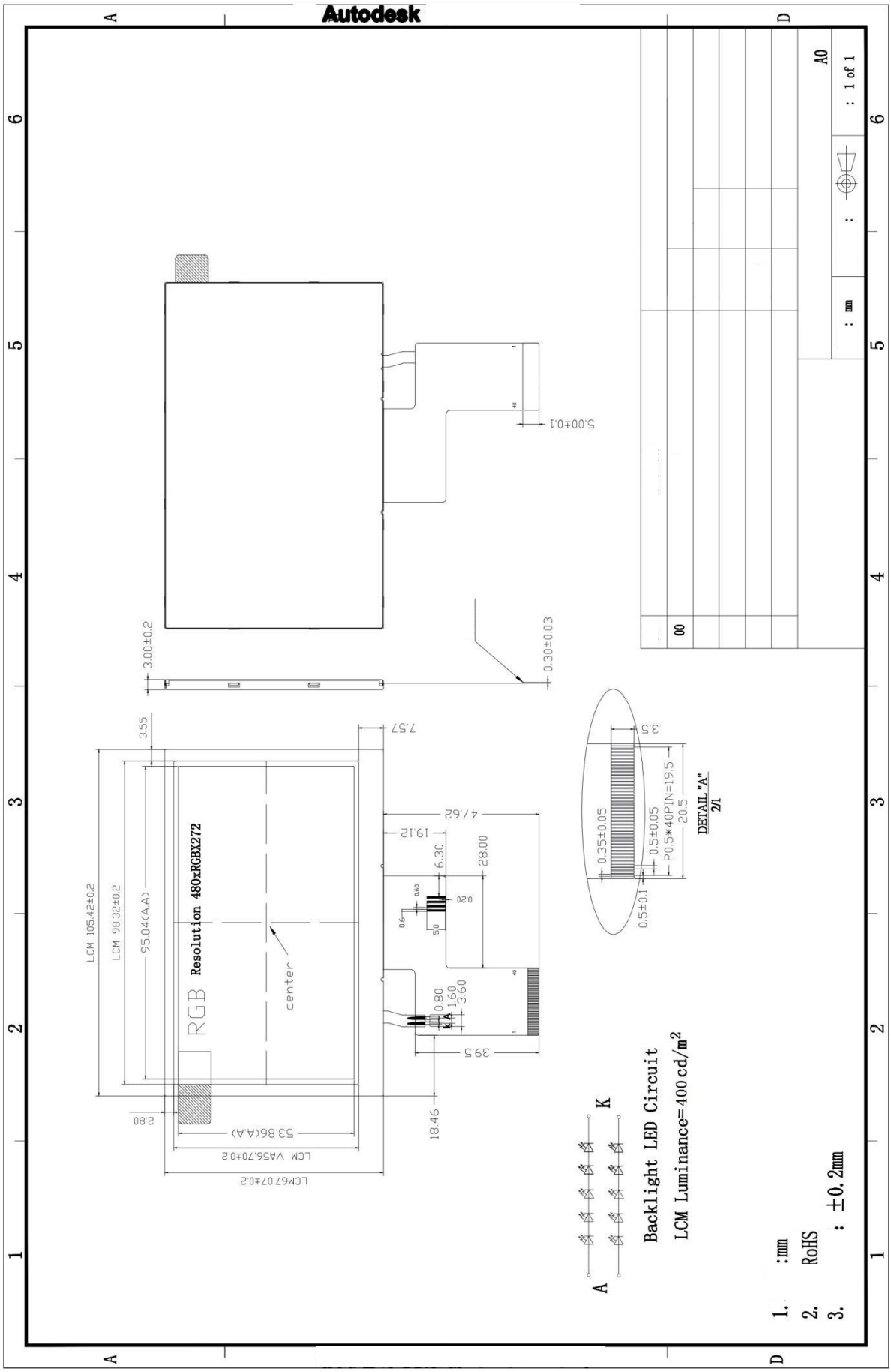
1. Be sure to ground module before turning on power or operating module.
2. Do not apply voltage which exceeds the absolute maximum rating value.

7.4. Storage

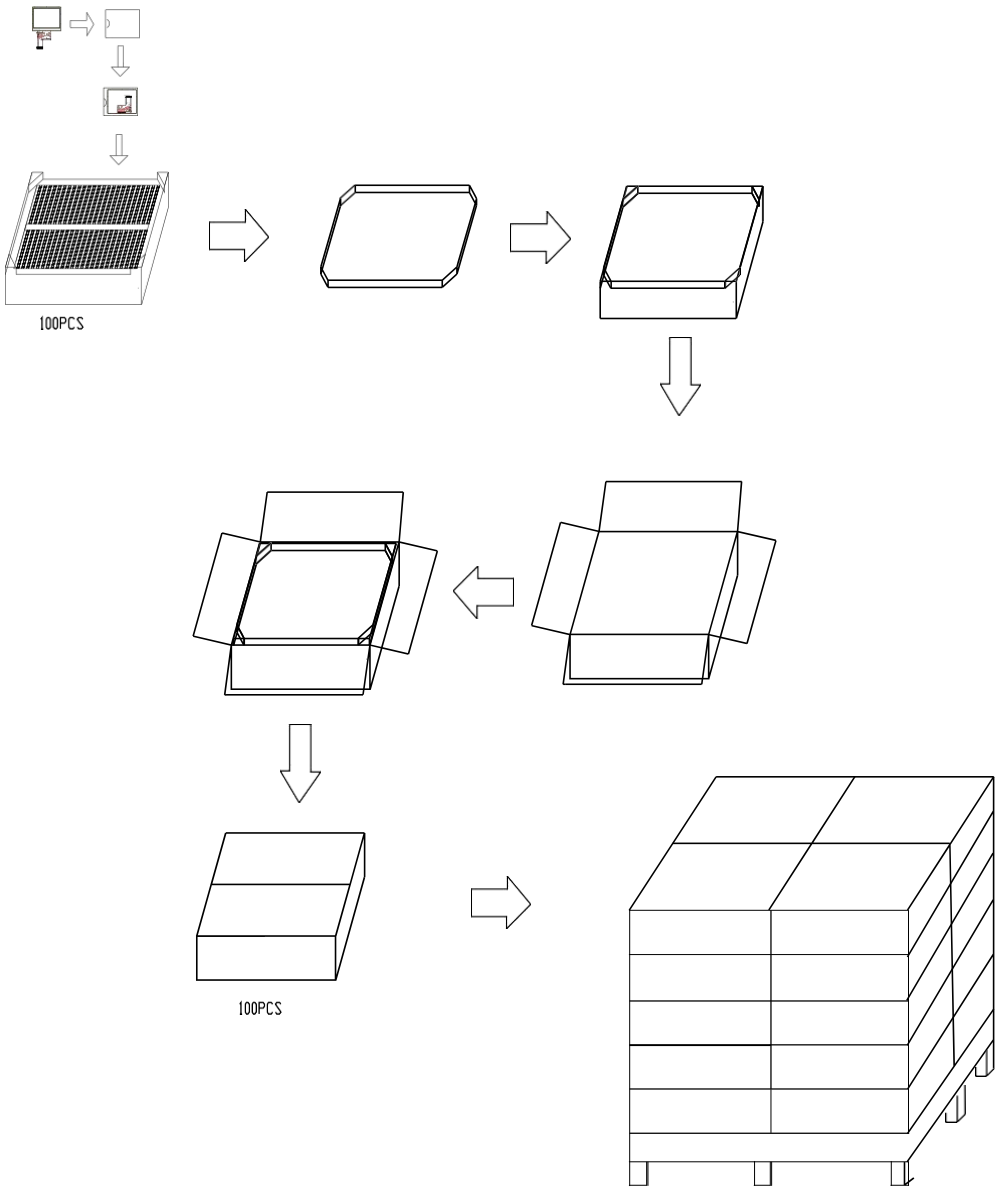
1. Store the module in a dark room where must keep at 25±10℃ and 65%RH or less.
2. Do not store the module in surroundings containing organic solvent or corrosive gas.
3. Store the module in an anti-electrostatic container or bag.

7.5. Cleaning

1. Do not wipe the polarizer with dry cloth. It might cause scratch.
2. Only usea softsloth with IPAto wipe the polarizer, other chemicals might permanent daage to the polarizer


Autodesk
RGB Resolution 480xRGBX272
center
LCM 105.42±0.2
LCM 98.32±0.2
95.04(A.A)
53.86(A.A)
LCM VA56.70±0.2
LCM67.07±0.2
2.80
3.55
7.57
3.00±0.2
0.30±0.03
5.00±0.1
47.62
19.12
6.30
28.00
39.5
18.46
0.80
1.60
3.60
0.35±0.05
0.5±0.05
P0.5*40PIN=19.5
20.5
3.5
0.5±0.1
DETAIL "A"
2/1
A
K
Backlight LED Circuit
LCM Luminance=400 cd/m²
1. :mm
2. RoHS
3. : ±0.2mm
00
A0
: mm
: 1 of 1

9.Packaging Drawing



9.Package Drawing

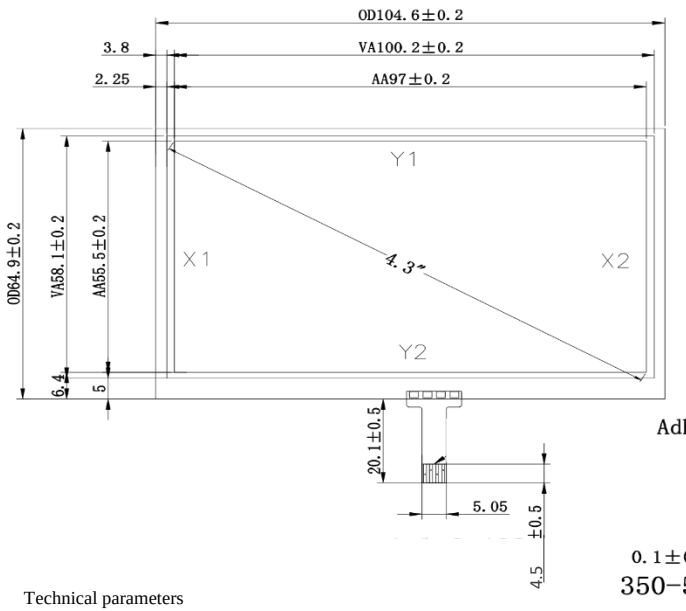
9.1 Package Material Table

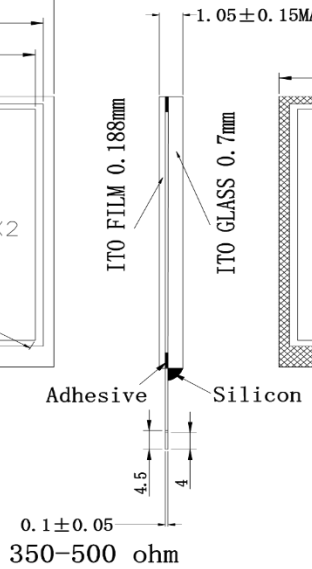
No.	Item	Model (Material)	Dimensins (mm)	Quantity (pcs)	Remark
1	LCM Module	AGO_043J2-NN-N	105.4×67.07×3	120	
2	Dust-Proof Bag	PE	148×95×0.5	120	
3	Bubble Chamber	Red PUF	500×290×110	1	
4	Bubble Chamber Lid	Red PUF	500×290×20	1	
5	Carton	Cor Rugated Paper	510×310×125	1	

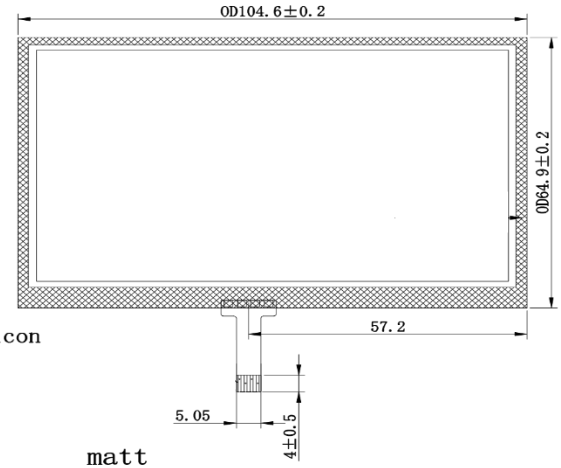
9.2. Packaging Quantity

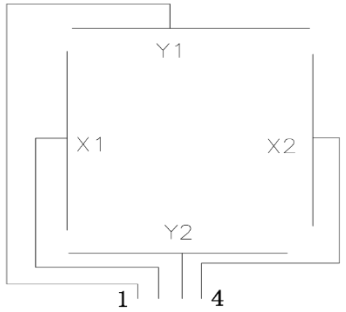
(1) LCM quantity per Partition: 2Rows x 60quantity per Row = 120 pcs
(2) Total LCM quantity in Carton: 1 layer x 120 pcs per Partition = 120 pcs

REV	MARK	HISTORY	DATE	NAME









PIN	WIRING
1	Y1
2	X1
3	Y2
4	X2

Technical parameters

1. Up wire material: 0.188mm
2. Down wire material: 0.7mm
3. Transmittance: ≥78%
4. Linear value: ≤1.5%
5. Connection: FPC solder
6. Resistant left & right: 400-1000Ω
   Resistant up & down: 100-340 Ω
7. Operation temperature: -20ºC ~+70 ºC
   Storage temperature: -30ºC ~+80 ºC
8. Hardness: 3H
9. Operation pressure: MAX 120g
10. Response time: ≤10ms
11. Touch test:1,000,000 times
12. General tolerance: ±0.2mm

Unspecified Tolerance ±0.20	AGT-043J0-01	VERSION: VERSION
DESIGNED BY:		NO. 1 OF 1
CHECKED BY:		UNIT: MM
		DATE: 2019-12-15